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U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE

RMATION DISCLOSURE STATEMENT BY APPLICANT

ATTY. DOCKET MIC-39	NO. APPLICATION NO. 10/734,339
APPLICANT Seong-Hoon Lee	CONFIRMATION NO. Not Yet Assigned
FILING DATE December 11, 20	GROUP 28/1

U.S. PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
*	5,463,337	10/31/95	Leonowich	327	158	
Al-	6,194,947 B1	02/27/01	Lee et al.	327	359	
the	6,295,328 B1	09/25/01	Kim et al.	375	376	
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OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

EXAMINER INITIAL	
TH	Jong-Tae Kwak, <u>A Low Cost High Performance Register-Controlled Digital DLL for 1 Gbps x32 DDR SDRAM</u> , The 8th Korean Conference on Semiconductors, February 2001.
AU	Ramin Farjad-Rad, <u>A Low-Power Multiplying DLL for Low-Jitter Multigigahertz Clock Generation in Highly Integrated Digital Chips</u> , IEEE Journal of Solid-State Circuits, Vol. 37,. No. 12, December 2002, p. 1804-1812.

All references have been considered.

EXAMINER INITIAL

EXAMINER

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DATE CONSIDERED

12/8/04

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not conformance and not considered. Include copy of this form with next communication to applicant.